The Design of Smart Meters for Smart Grid

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Abstract

This paper designs a new type of smart meter that adopts the idea of modularization and STM32F103VET6 single-chip microcomputer is placed as CPU. The software and hardware design method of CPU control module, communication module, and the data acquisition and measurement module. The smart meter not only can realize data acquisition, ladder electricity price, settlement of electricity charges but also can realize the function of meter reading, monitoring and real-time communication.

Keywords

Smart meter, Power quality detection, Automatic data logging, STM32F103VET6.

1. Hardware design of smart meters

The new smart meter adopts the modular design idea, which can be divided into CPU module, voltage and current parameter acquisition and measurement module, storage module, power supply and clock module, communication module, key control, display module and so on^[1-3]. The overall structure of the system is shown in Figure 1.

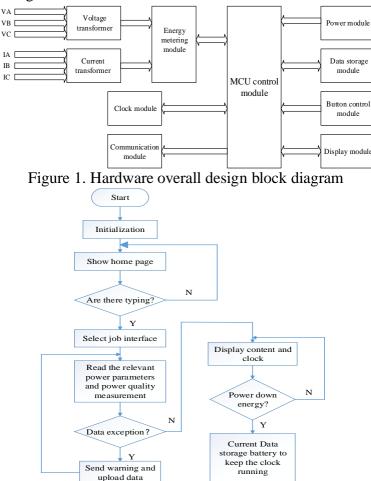


Figure 2. Software system flow chart

2. Software design of intelligent meters

Due to more features in the new smart meters and the high complexity of data process need to achieve, so we put forward to the modular design ideas^[4-7]. The overall structure of the software system is shown in Figure 2.

3. Performance Test

According to Q / GDW 362-2009 \langle Single - stage three - phase cost - controlled intelligent energy meter technical specification \rangle , using the standard energy meter method, the ambient temperature of 23 °C ±2 °C. The device in the test environment and smart meter shown in Figure 3.



Figure 3. The device of the smart meter hardware and meter reading

Using five and a half desktop digital multi-purpose table measures the power parameters of the meter. At the experimental input voltage: $3 \times 220 / 380V$, input current: 0A-5A, frequency 50Hz, measured AC voltage error of 0.06%, measurement of AC error of 0.14%, power factor of 1. Selection 900W adjustable load motor in a star connection leads A, B, C phase power can be given Quality data. It is based on respectively active and reactive power, voltage and current RMS, active reactive power, power factor and line frequency measurement^[8-9]. This paper only gives the voltage deviation, frequency deviation, power measurement data experimental measurement data, as shown in Table 1 ~ 3.

	Measured voltage (U/V)	Standard voltage (U/V)	Relative error	Voltage deviation (U∆)
А	220.03	220.2	0.08%	0.18%
В	219.36	219.6	0.01%	0.72%
С	219.80	220. 0	0.09%	0.36%

Table 1. Voltage Deviation Measurement Data

Table 2. Frequ	uency deviation	measurement data
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Phase numb er	Nominal frequency (f _N /Hz)	Measured frequency (f/Hz)	Frequency deviation (f _A)
A	50Hz	50.04	0.08%
В	50Hz	50.03	0.06%
С	50Hz	50.01	0.02%

Phase numb er	Active power (P/W)	Active power relative error	Apparent power (S/VA)
А	300.1	0.38%	300.1
В	300.4	0.29%	300.4
С	300.9	0.23%	300.9

Table 3. Power measurement data

The data from the above indicators are within the allowable requirements to meet the overall design of the basic requirements.

4. Conclusion

This paper designs a new smart meter for smart grid. The data of the power quality data and the remote meter reading are tested. The results show that the data indexes of power quality are within the allowable requirements, and achieve a new smart meter power quality testing capabilities. It meets the design specifications.

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